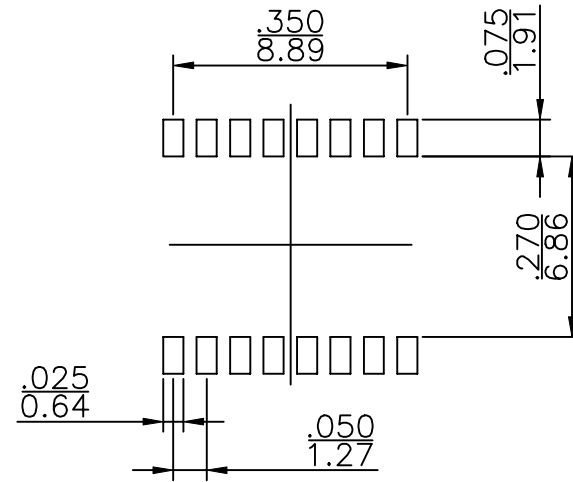
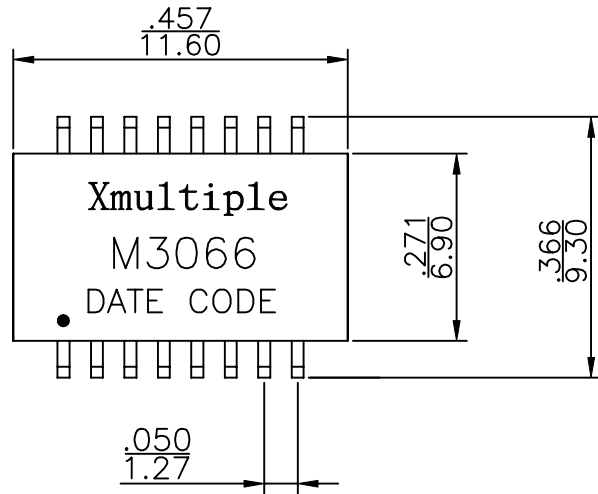
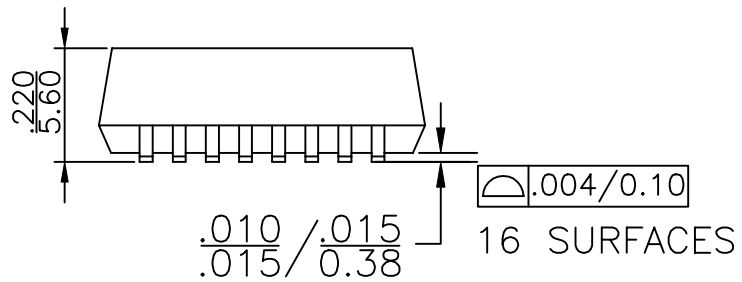


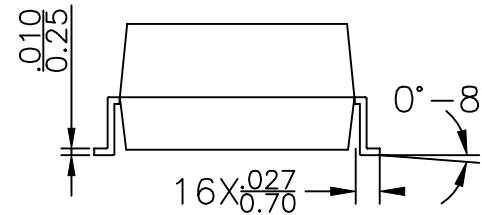
MECHANICALS



SUGGESTED PAD LAYOUT



FINAL OUTLINE



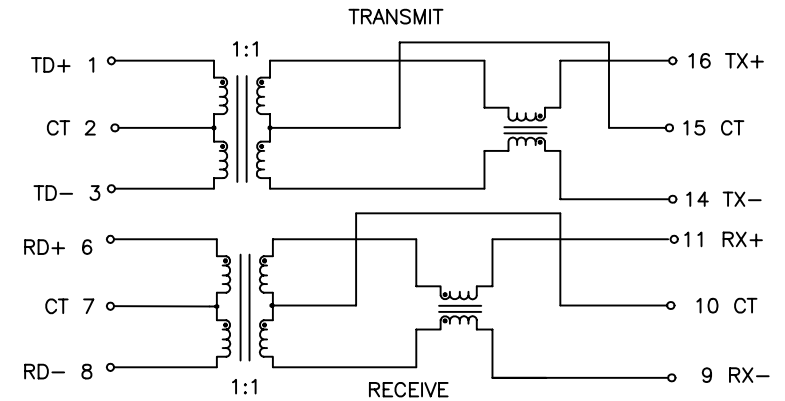
Dimensions:Inch[mm] all tolerances:0.01[0.25]

<h1>X MULTIPLE</h1>		CONNECTING THE INFORMATION AGE
XMULTIPLE ASIA 4F, No. 347, Yang Guang St., Neihu Chiu, Taipei, Taiwan 11475	XMULTIPLE USA 543 Country Club Drive #B 128 Simi Valley, CA 93065	CUSTOMER/NO:
THIS DRAWING IS A CONTROLLED DOCUMENT.		DWG NO: XTFZ-M3066
TITLE: FAST ETHERNET 10/100 BASE-TX TRANSFORMER MODULES.		DRAWN: JEFF
PART NO: XTFZ-M3066		CHKD: FUTIEN
	DIMENSIONS inch [mm]	SHEET 1 of 2
	REV 01	DATE: 24/02/04

TEST NOTES:(25±5°C)

- 1.TR:(100KHz,0.1V);
 PINS:(1,3):(16,14)=1:1±3%
 PINS:(6,8):(11,9)=1:1±3%
- 2.LX:(100KHz,100mV,8mA, DC Bias)
 PINS:(1,3),(6,8)=350uH MIN
- 3.DCR:
 PINS:(16,14),(11,9) =1.2 OHMS MAX
 PINS:(1,3),(6,8) =1.0 OHMS MAX
- 4.HIPOT:
 PINS:(1,2,3)TO(16,15,14)=1500VAC FOR 60 SECONDS
 PINS:(6,7,8)TO(11,10,9)=1500VAC FOR 60 SECONDS
- 5.INSERTION LOSS:
 -1.1dB MAXIMUM AT 0.1MHz TO 100MHz;
- 6.RETURN LOSS:
 -16dB MINIMUM AT 1MHz TO 40MHz;
 -14dB MINIMUM AT 40MHz TO 50MHz;
 -13dB MINIMUM AT 50MHz TO 60MHz;
 -10dB MINIMUM AT 60MHz TO 80MHz
- 7.CROSS TALK:
 -50dB MINIMUM AT 1MHz TO 30MHz;
 -40dB MINIMUM AT 30MHz TO 50MHz;
 -33dB MINIMUM AT 50MHz TO 100MHz;
- 8.DIFFERENTIAL TO COMMON MODE REJECTION:
 -38dB MINIMUM AT 1MHz TO 60MHz;
 -30dB MINIMUM AT 60MHz TO 100MHz

SCHEMATIC



X MULTIPLE		CONNECTING THE INFORMATION AGE	
X MULTIPLE ASIA 4F, No. 347, Yang Guang St., Neihu Chiu, Taipei, Taiwan 11475		X MULTIPLE USA 543 Country Club Drive #B 128 Simi Valley, CA 93065	
THIS DRAWING IS A CONTROLLED DOCUMENT.		CUSTOMER/NO:	
TITLE: FAST ETHERNET 10/100 BASE-TX TRANSFORMER MODULES.		DWG NO: XTFZ-M3066	
PART NO: XTFZ-M3066		DRAWN: JEFF	
		CHKD: FUTIEN	
		DATE: 24/02/04	
DIMENSIONS		SHEET	REV
inch [mm]		2 of 2	01